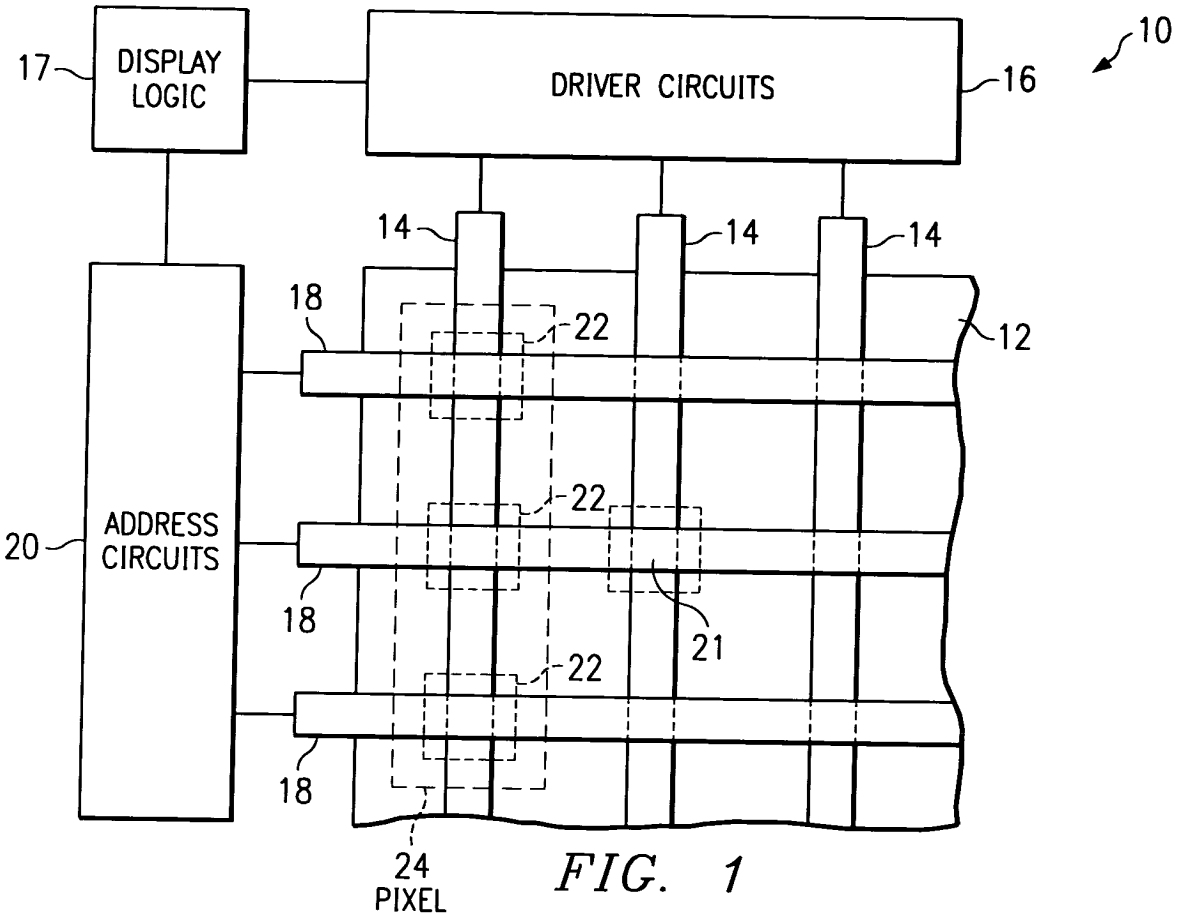
10
17
DISPLAY LOGIC
DRIVER CIRCUITS
16
14
14
14
18
22
12
ADDRESS CIRCUITS
20
22
18
21
22
18
24
PIXEL

FIG. 1

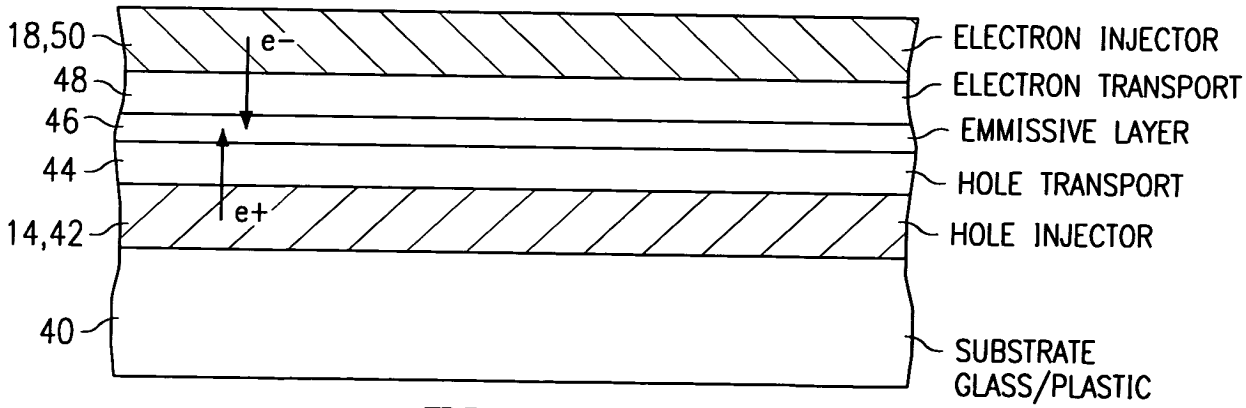
18,50
e−
48
46
44
e+
14,42
40
ELECTRON INJECTOR
ELECTRON TRANSPORT
EMMISSIVE LAYER
HOLE TRANSPORT
HOLE INJECTOR
SUBSTRATE
GLASS/PLASTIC

FIG. 2

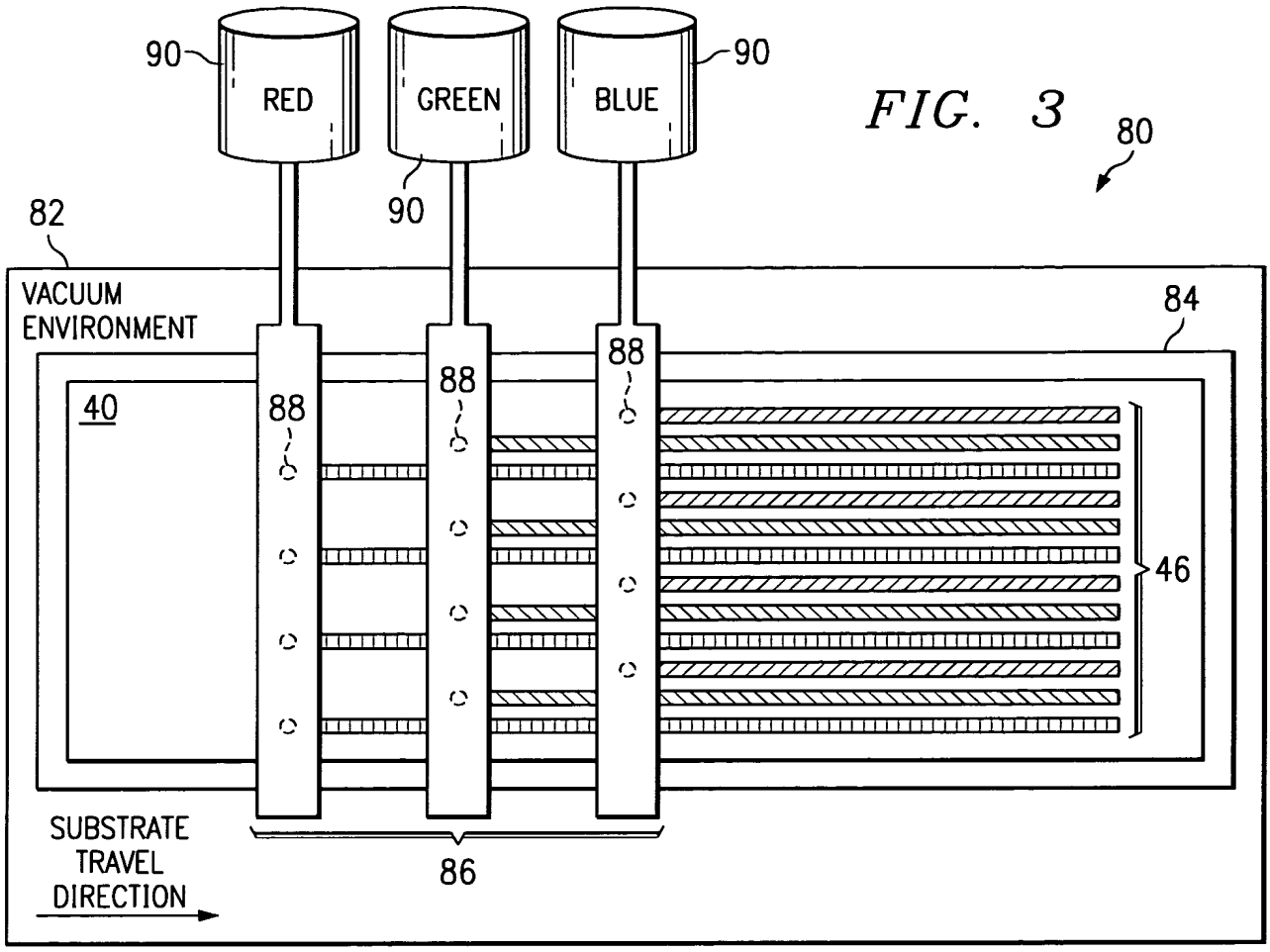
90
RED
GREEN
BLUE
90
90
80
82
VACUUM
ENVIRONMENT
84
40
88
88
88
46
SUBSTRATE
TRAVEL
DIRECTION
86

FIG. 3

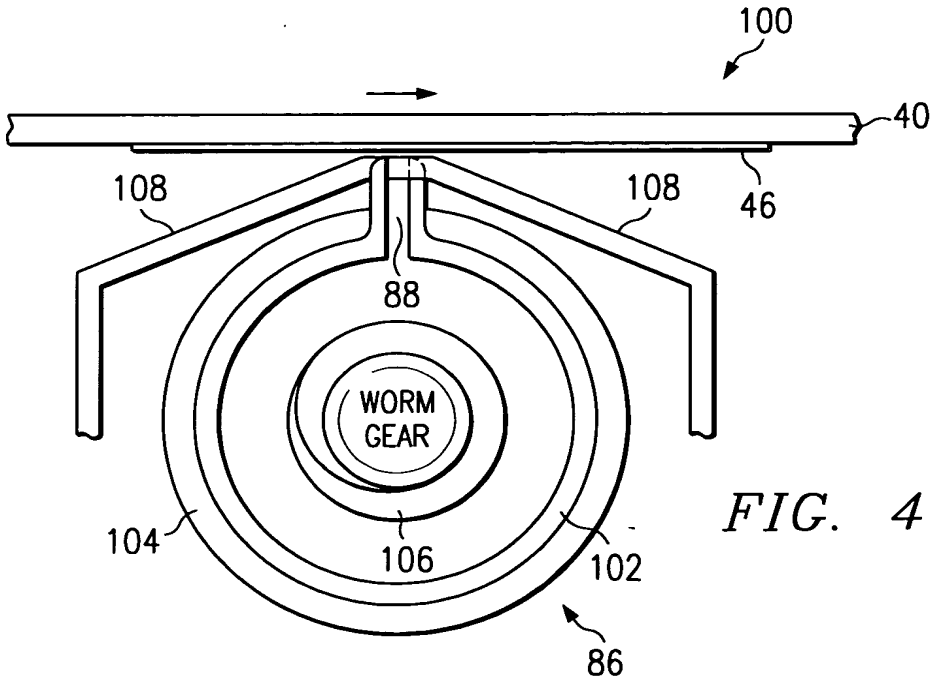
100
40
108
108
46
88
WORM
GEAR
104
106
102
86

FIG. 4

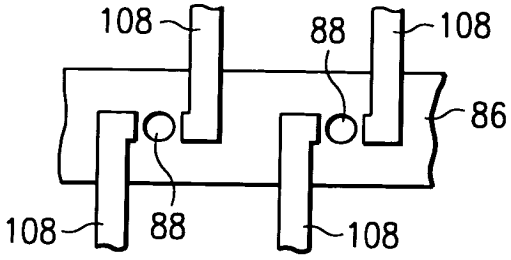
108
88
108
86
108
88
108

FIG. 5

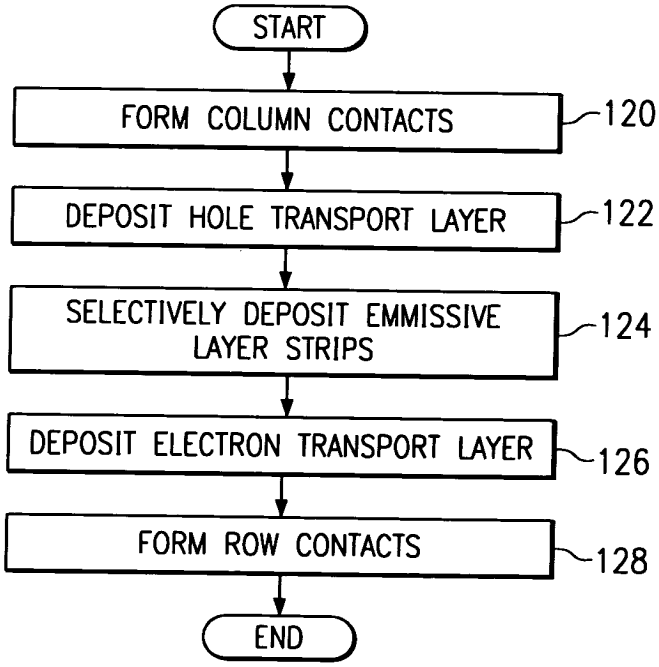
START
FORM COLUMN CONTACTS
120
DEPOSIT HOLE TRANSPORT LAYER
122
SELECTIVELY DEPOSIT EMMISSIVE LAYER STRIPS
124
DEPOSIT ELECTRON TRANSPORT LAYER
126
FORM ROW CONTACTS
128
END

FIG. 6